

FORM PTO-1449	O I P E P A T E N T & T R A D E M A R K O F F I C E D E C 2 7 2004	Atty. Docket No. ATM-276	Serial No. 10/810,033
LIST OF PRIOR ART CITED BY APPLICANT		Applicants: Jean-Michel Daga et al.	
		Filing Date: March 26, 2004	Group: 2816

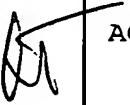
U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Name	Class	Sub Class	Filing Date
AA						
AB						
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AH						
AI						
AJ						

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Country	Class	Sub Class	Translation Yes No
AK						
AL						
AM						
AN						

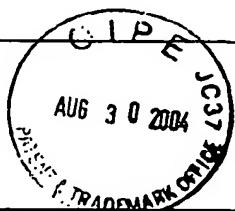
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AO	Article entitled "A New High Efficiency CMOS Voltage Doubler" by Pierre Favrat et al., Swiss Federal Institute of Technology Electronics Labs EL-LEG, Ecublens, CH-1015 Lausanne, Switzerland and Mead Microelectronics Engineering Applications and Design SA, Venoge 7, CH-1025 St. Sulpice, Switzerland, IEEE 1997 Custom Integrated Circuits Conference, pages 259-262 (0-7803-3669-0).
	AP	
EXAMINER:		
	DATE CONSIDERED: 9/15/05	

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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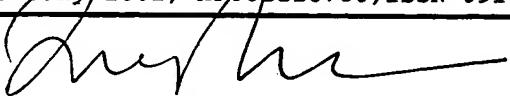
FOREIGN PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Country	Class	Sub Class	Translation Yes	No
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AL							
AM							
AN							

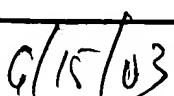
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AO	S-C Lee et al., A Low-Ripple Switched-Capacitor DC-DC up Converter for Low-Voltage Applications, IEICE Transactions on Elect., Instit. Electronics Information and Comm. Eng., August 2001, Vol. E84-C, No. 8, pages 1100-1103, XP001107649, ISSN 0916-8524.
	AP	K-S Min et al., CMOS Charge Pumps Using Cross-Coupled charge Transfer Switches with Improved Voltage Pumping Gain and Low Gate-Oxide Stress for Low-Voltage Memory Circuits, IEICE Transactions on Electron., Instit. Electronics Information and Comm. Eng., January 2002, Vol. E85-C, No. 1, January 2002, XP001116786, ISSN 0916-8524.

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FORM PTO-1449	3/24/2004	Atty. Docket No. ATM-276	Serial No. 10/810,033
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Examiner Initial*	Document Number	Grant Date	Country	Class	Sub Class	Translation Yes No
	AK					
	AL					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AM	Hongchin Lin et al., "New Four-Phase Generation Circuits for Low-Voltage Charge Pumps", IEEE, 2001, pp. 504-507.
AN	
AO	

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DATE CONSIDERED:

6/15/03

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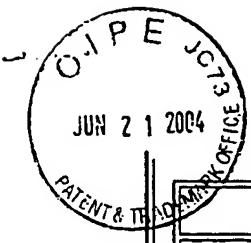
ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	HIGH EFFICIENCY, LOW COST, CHARGE PUMP CIRCUIT											
Application Number:	10/810033 											
Confirmation Number:	8427											
First Named Applicant:	Jean-Michel Daga											
Attorney Docket Number:	ATM-276											
Art Unit:	2816											
Search string:	(4581546 or 5196996 or 5352936 or 5546296 or 5818289 or 5874850 or 5889428 or 6163487 or 6166585 or 6353356 or 6429723 or 6437637 or 6448841 or 6466079 or 6515535).pn.											
US Patent Documents												
Note: Applicant is not required to submit a paper copy of cited US Patent Documents												
Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass					
1	4581546	1986-04-08	Allan	A1	307	297						
2	5196996	1993-03-23	Oh	A1	363	60						
3	5352936	1994-10-04	Allen	A1	307	304						
4	5546296	1996-08-13	Savignac et al.	A1	363	60						
5	5818289	1998-10-06	Chevallier et al.	A1	327	536						
6	5874850	1999-02-23	Pulvirenti et al.	A1	327	536						
7	5889428	1999-03-30	Young	A1	327	536						
8	6163487	2000-12-19	Ghilardelli	A1	365	189.09						
9	6166585	2000-12-26	Bazzani	A1	327	536						
10	6353356	2002-03-05	Liu	A1	327	536						
11	6429723	2002-08-06	Hastings	A1	327	536						
12	6437637	2002-08-20	Myono	A1	327	536						
13	6448841	2002-09-10	Milazzo	A1	327	536						
14	6466079	2002-10-15	Kushnarenko	A1	327	536						
15	6515535	2003-02-04	Myono	A1	327	536						

Signature



Examiner Name	Date
	6/15/05